

## SB7560S 75A SCR

### FEATURES

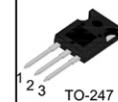
- High switching performance
- High voltage capacity
- Very high current surge capability

### APPLICATIONS

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

### Parameters Summary

V<sub>D</sub>(V<sub>H</sub>)=200V, I<sub>(RMS)</sub>=75A, I<sub>GT</sub>=50mA



### ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40 ~ 150	°C
Operating junction temperature range	T <sub>op</sub>	-40 ~ 125	°C
Repetitive peak off-state voltage (T = 25°C)	V <sub>DRM</sub>	1200/1000	V
Repetitive peak reverse voltage (T = 25°C)	V <sub>RDM</sub>	1000/1000	V
Non repetitive surge peak Off-state voltage	V <sub>DSM</sub>	V <sub>DRM</sub> +100	V
Non repetitive peak reverse voltage	V <sub>DRM</sub>	V <sub>RDM</sub> +100	V
RMS on-state current (T = 100°C)	I <sub>T(RMS)</sub>	75	A
Non repetitive surge peak on-state current	I <sub>TSM</sub>	700	A
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	2450	A <sup>2</sup> s
Critical rate of rise of on-state current (I = 2×IGT, tr ≤ 100 ns)	di/dt	150	A/μS
Peak gate current	I <sub>GM</sub>	5	A
Average gate power dissipation	P <sub>G(AV)</sub>	2	W

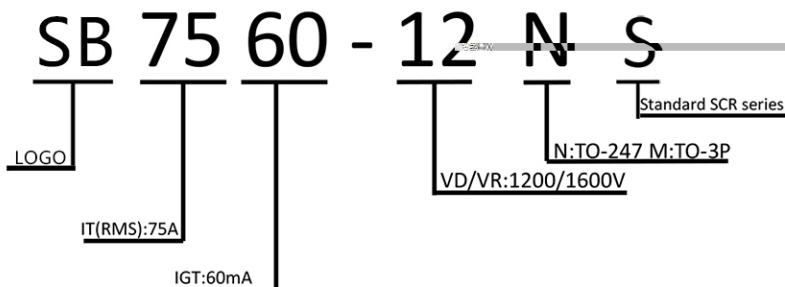
### Thermal Resistances

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (DC)	TO-3P	0.60
		TO-247	0.55
			°C/W

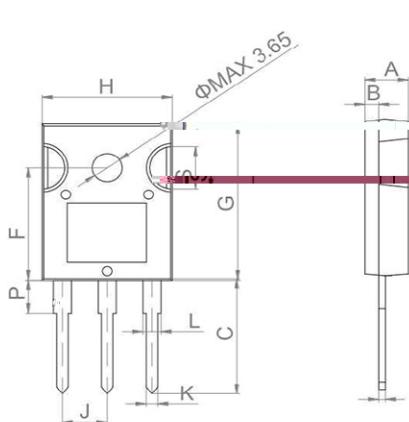
Symbol	Unit	Description	Value	Notes
$V_{GS}$	V	Gate-to-Source Voltage	-14 V	R = 14V/2
$V_{GP}$	V	Gate-to-Drain Voltage	-14 V	R = 14V/2
$I_L$	A	Load Current	1 A	R = 10Ω
$dV/dI$	V/A	Slope of the I-V curve	14 V/A	R = 14V/2

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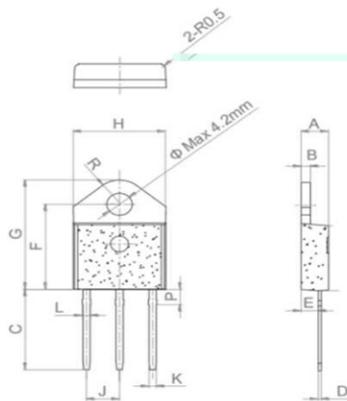
## Ordering Information Scheme



## TO-247 Package Mechanical Data



## TO-3P Package Mechanical Data



Ref.	Dimensions					
	Millimeters			Inches		
	Min	Max	Min	Max	Min	Max
A	4.40		4.60		0.173	
B	1.40		1.60		0.055	
C	15.48		15.88		0.610	
D	0.50		0.70		0.019	
E	2.70		2.90		0.106	
F	15.92		16.32		0.626	
G	20.27		20.67		0.798	
H	15.15		15.35		0.590	
J		5.45				0.214
K	1.10		1.30		0.043	
L	1.15		1.35		0.045	
P	2.68		3.08		0.105	
R		4.20				0.165

FIG.1 Maximum power dissipation versus on-state current

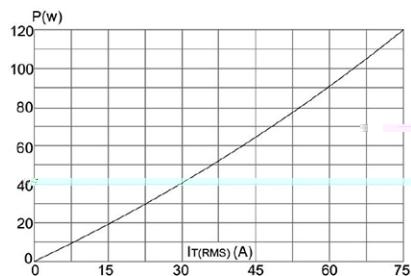


FIG.3: Surge peak on-state current versus number of cycles

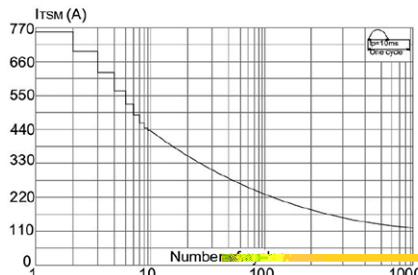


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$ , and corresponding value of  $I_2 t$  ( $dI/dt < 50\text{A}/\mu\text{s}$ )

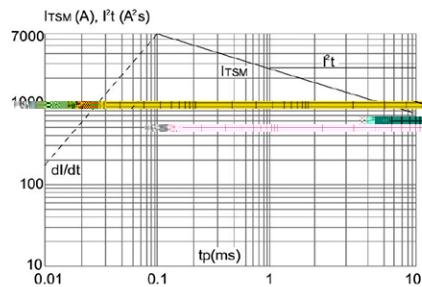


FIG.2: on-state current versus case temperature

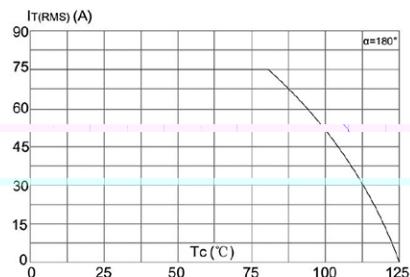


FIG.4: On-state characteristics (maximum values)

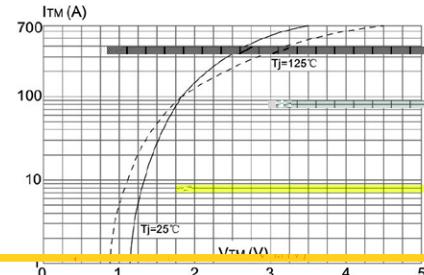


FIG.6: Relative variations of gate trigger current holding current and latching current versus junction temperature

